Computing - Big Impact in the 21st Century



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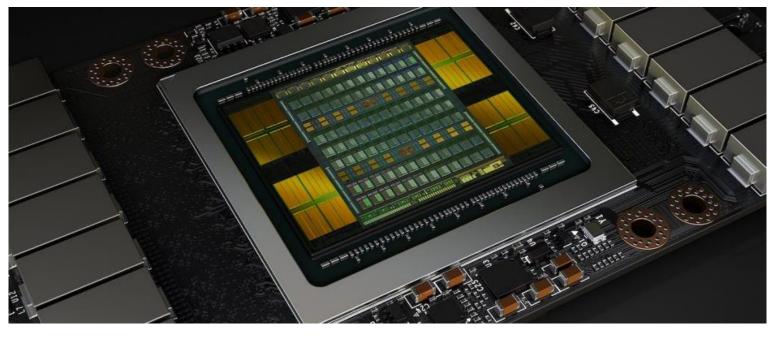


1988 Start of the Hwu Family

2016 Yale Wins Franklin Medal







134K vs. 12.1B transistors

12 MHz vs. 1.1 GHz

1.5 um vs. 12 nm

2.7 MIPS (needs 287 for FP) vs. 14 TFLOPS

1MB DRAM vs. 16GB HBM





The Industry Landscape

- Apple II
- Sony, DEC, IBM, Intel and Microsoft

• ...

- iPhone X
- Samsung, Apple, NVIDIA,
 Amazon, Google, and Facebook

• ...



Innovation

A high-value concept in the right historical context

Important Innovations in Recent History

- Telescope
- Microscope
- Electricity
- Telephone
- Medical imaging
- Electrical Motor
- Automobiles
- Airplane

- Credit cards
- Radar
- Clean Energy
- Mobile phones
- Internet and search engine
- eCommerce
- Social media
- GPS



Future innovations will rely heavily on computing













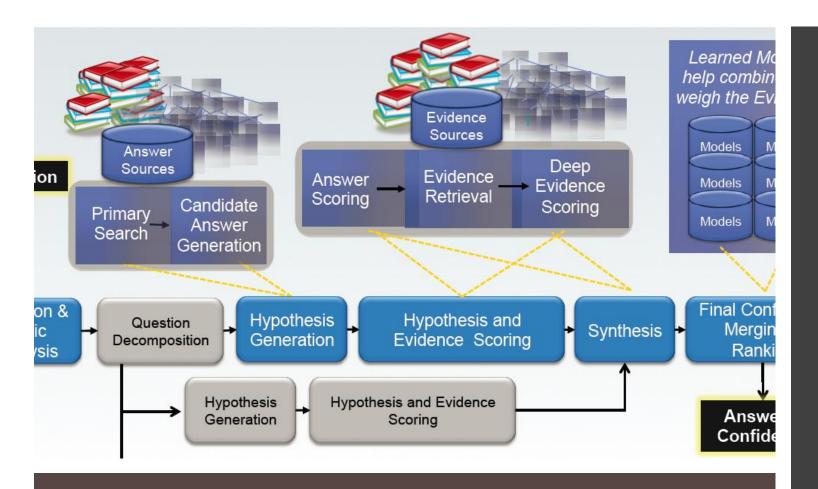
#iq2uslive





Hardware for Watson Jeopardy! 2011

- 90 x IBM Power 750 servers
 - 2880 Power7 cores
 - 3.55 GHz clock
 - 80 TeraFLOPS
- 15 Terabytes of DRAM
- 20 Terabytes of disk
- 10 Gb Ethernet network
- > 100,000 Watt power



Software for Watson Jeopardy! 2011

"Watson DeepQA generates and scores many hypotheses using an extensible collection of Natural Language Processing, Machine Learning and Reasoning Algorithms.

These gather and weigh evidence over both unstructured and structured content to determine the answer with the best confidence."

Novelty vs. Great Product

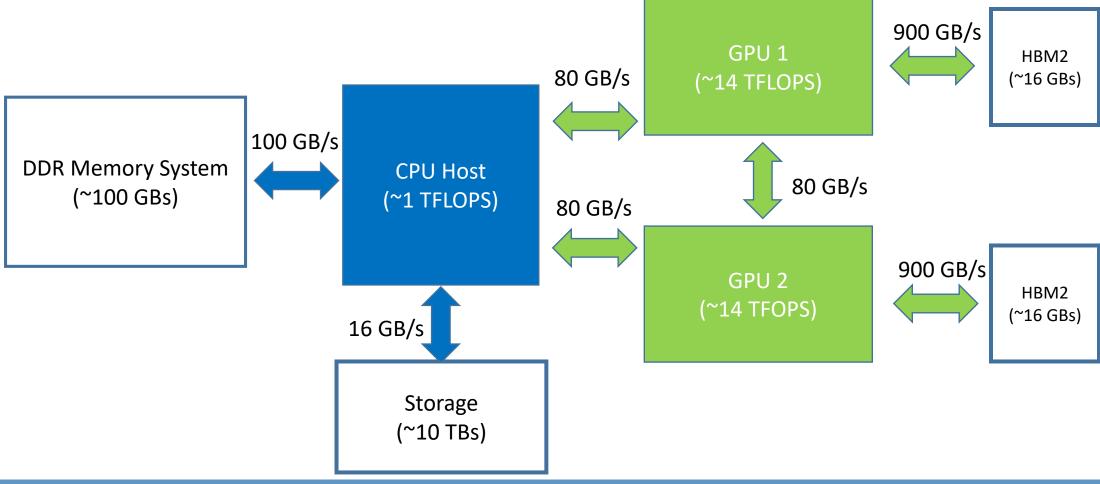


German *Flocken Elektrowagen* of 1888, regarded as the first electric car of the world



American Tesla Model X of 2017, whose producer is worth more than GM and Ford

A Simplified View of IBM Newell with NVIDIA Volta GPUs



Data Access Challenge (HBM2)

Volta 14.03 SP TFLOPS 900 GB/S

225 Giga SP operands/cycle

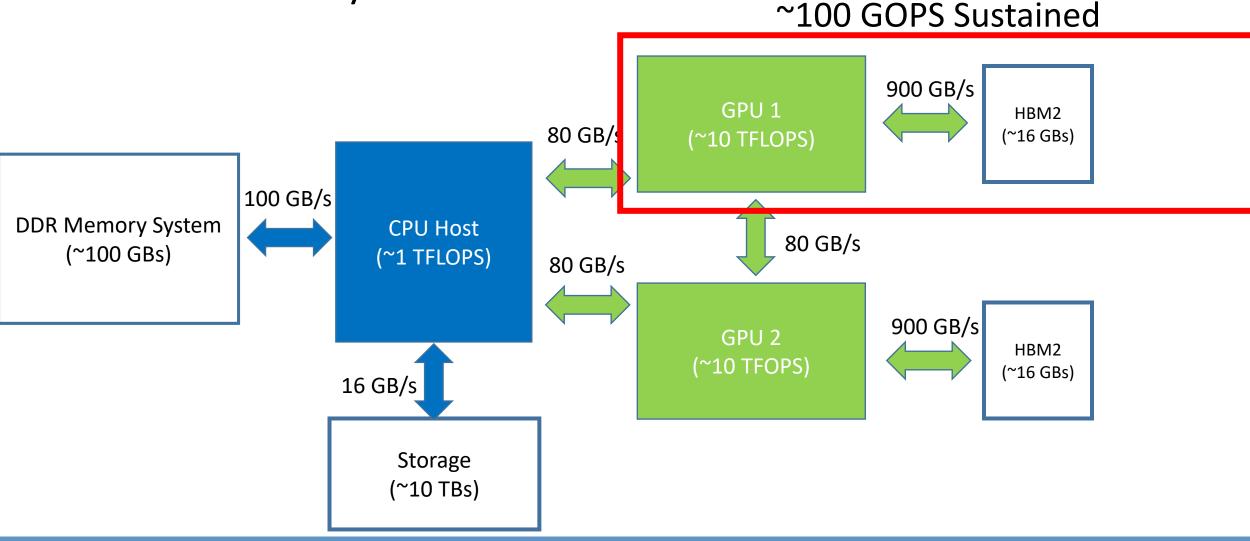
HBM2 16 GB

Each operands must be used **62.3 times** once fetched to achieve peak FLOPS rate.

or

Sustain < 1.6% of peak without data reuse

Graph Analytics Example – if graph fits into GPU Memory



Data Access Challenge (Host DDR3)

Volta 14.03 SP TFLOPS 80 GB/S

NVLINK

Host DDR3 128-512 GB

20 Giga SP operands/cycle

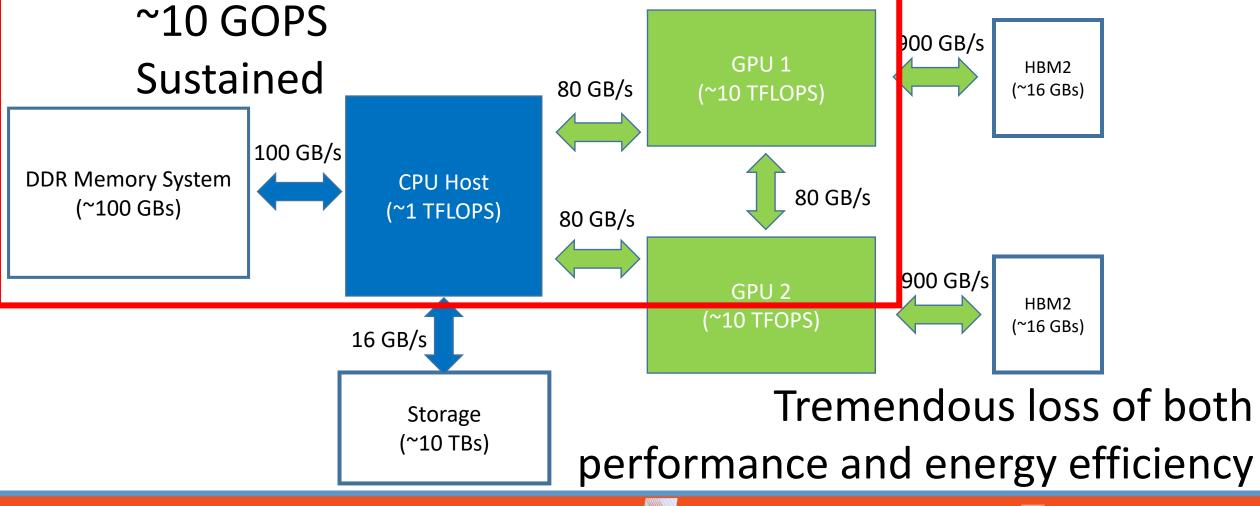
Each operands must be used **700 times** once fetched to achieve peak FLOPS rate.

or

Sustain < 0.14% peak without data reuse



Graph Analytics Example – if graph fits into Host DDR Memory



Data Access Challenge (FLASH SSD)

Volta 14.03 SP TFLOPS 16 GB/S
PCle 3

4 Giga SP operands/cycle

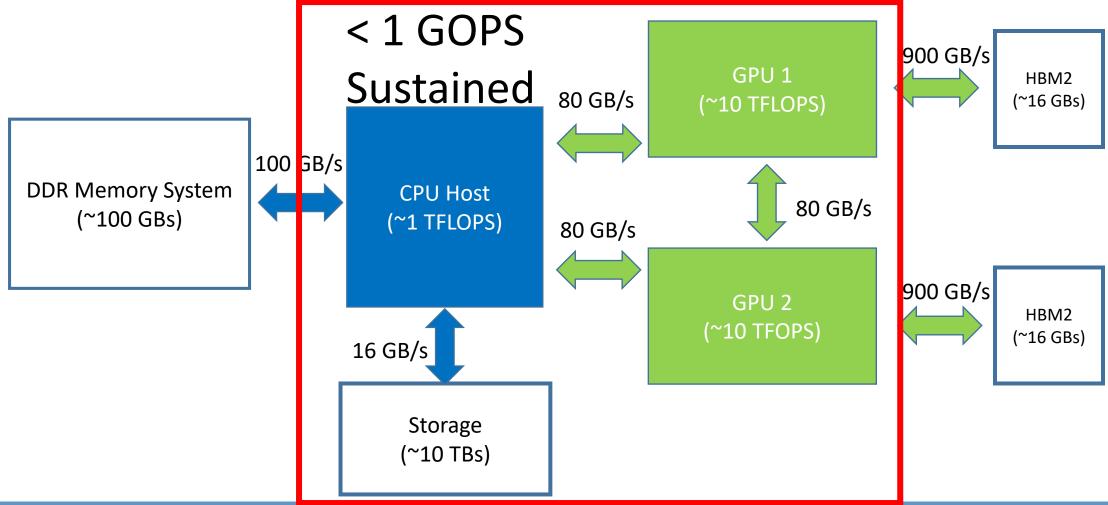
FLASH 1,000-5,000 GB

Each operands must be used **3,507 times** once fetched to achieve peak FLOPS rate.

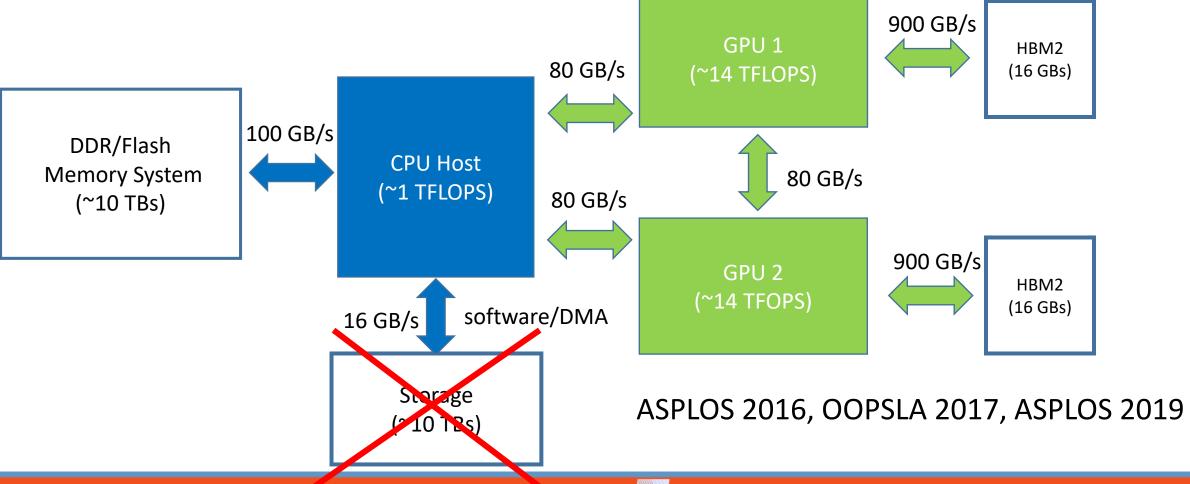
or

Sustain < 0.03% of peak without data reuse

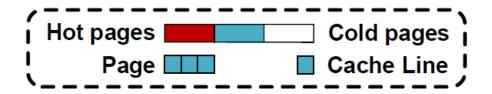
Graph Analytics Example – if graph is accessed from storage

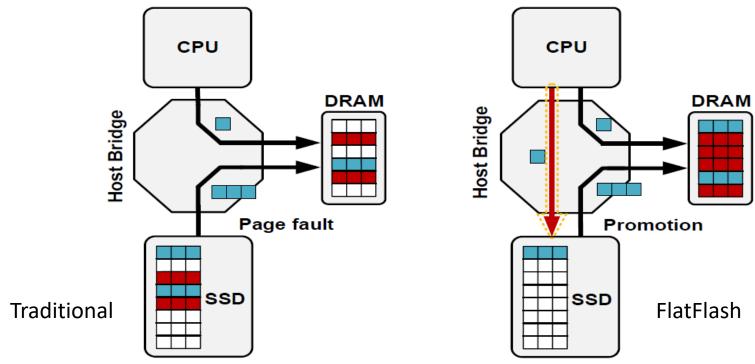


Erudite Vision: remove file system from data access path



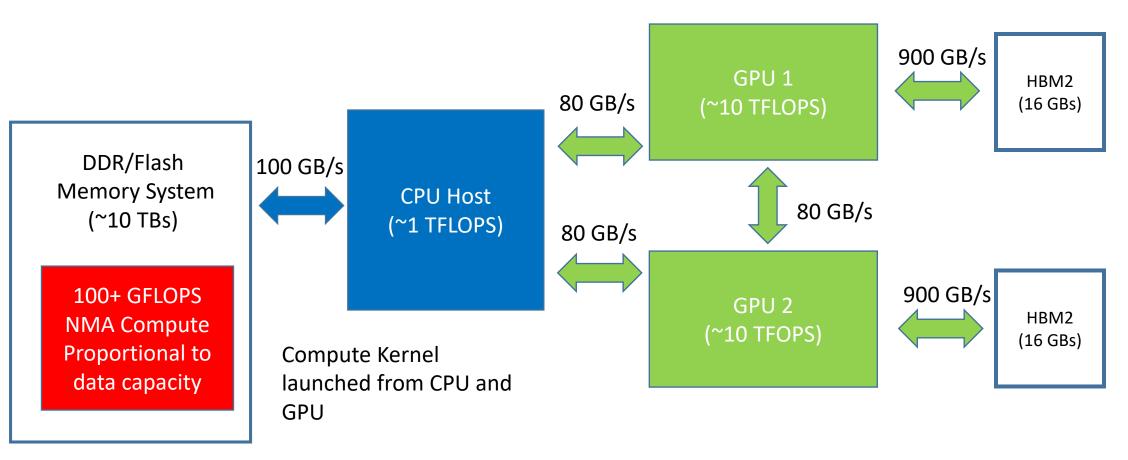
FlatFlash – Storage-class Memory





ASPLOS '19 – Abdula, Mailthody, Quresh, Xiong, Huang, Kim, Hwu

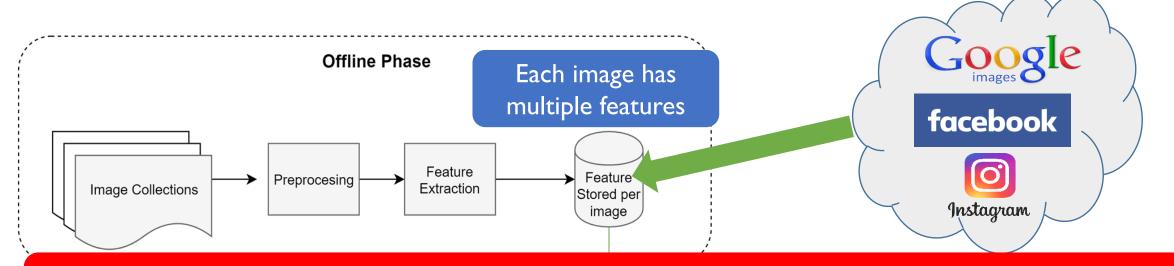
Erudite Vision: place NMA compute inside memory system



IEEE MICRO 2017



DeepStore: In-Storage Acceleration for Intelligent Image Search



Hard to Build Index for Intelligent Image-Based Search Applications

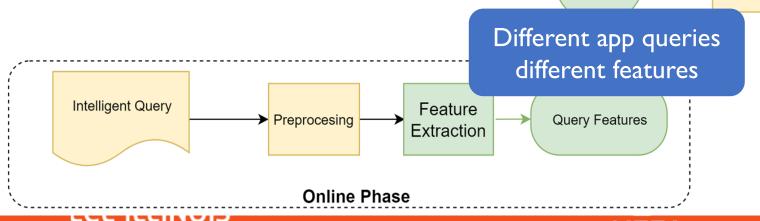


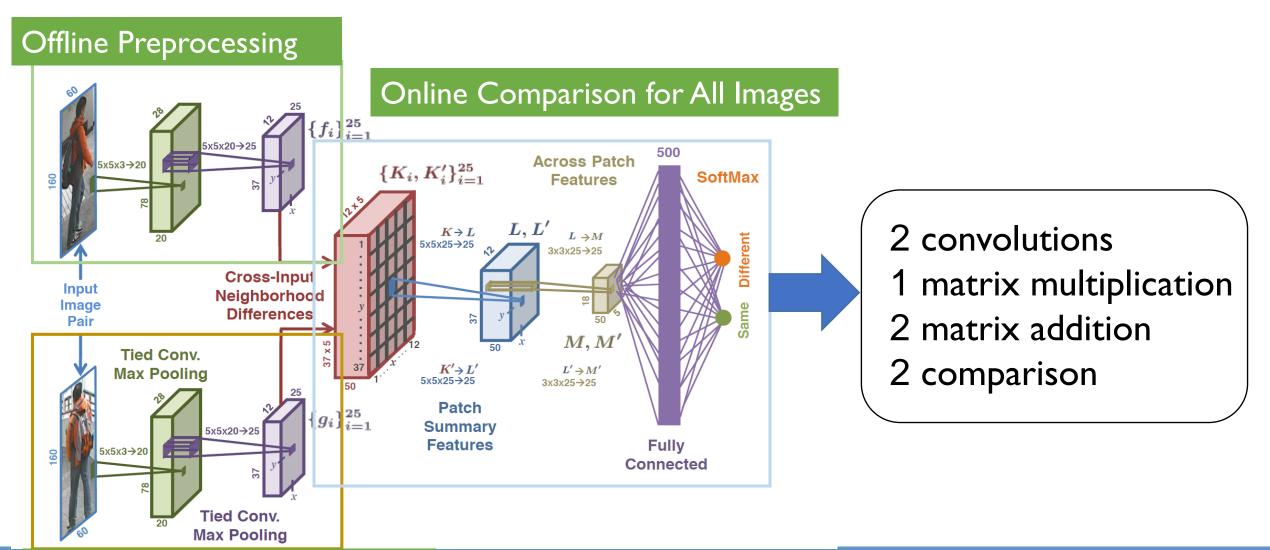


Image-based Apps

L∥ILLINO2**7** S

NCSA

Case Study: Person Re-Identification



Some Predictions for Yale:100

- Prominent Companies will be very different from today.
- Prominent products will be very different from today.
- The role of universities will be very different from today.
- We will still complain about ISCA and MICRO reviews...
- We will still come to Barcelona.

Way to go, Yale!

